

# **Controlling Base-SiO<sub>2</sub> Density of Low-Leakage 1.6 nm Gate-SiON for High-Performance and Highly Reliable n/pFETs**

M. Togo, K. Watanabe, M. Terai, S. Kimura, A. Morioka, T. Yamamoto, T. Tatsumi, and T. Mogami

Silicon Systems Research Labs., NEC Corporation  
1120 Shimokuzawa, Sagamihara, Kanagawa 229-1198, Japan

## **Abstract**

We will report the importance of high-density base-SiO<sub>2</sub> for nitridation, and demonstrate a low-leakage and highly reliable 1.6 nm gate-SiON without performance degradation in n/pFETs using the radical process. It was found that the high-density 1.6 nm SiO<sub>2</sub> is ten times more reliable than the low-density SiO<sub>2</sub> in n/pFETs and is suitable for the base layer of radical nitridation due to maintaining the surface nitridation of the SiO<sub>2</sub> and the ideal SiON/Si-substrate interface. The 1.6 nm SiON with the high-density base-SiO<sub>2</sub> produces comparable drivability in n/pFETs, and has one and half orders of magnitude less gate leakage in nFETs, one order of magnitude less gate leakage in pFETs, and ten times more reliable in n/pFETs than the 1.6 nm SiO<sub>2</sub>.