## A Still Image Encoder Based on Adaptive Resolution Vector Quantization with Needless Calculation Elimination Architecture Realizing Compression Ratio over 1/200

M.Fujibayashi, T.Nozawa, T.Nakayama, K.Mochizuki, M.Konda<sup>1</sup>,

K.Kotani, S.Sugawa and T.Ohmi<sup>2</sup>

Department of Electronic Engineering, Graduate School of Engineering, Tohoku University <sup>1</sup>I & F Co.Ltd. <sup>2</sup>New Industry Creation Hatchery Center, Tohoku University Aza-Aoba 05, Aramaki, Aoba-ku, Sendai 980-8579, JAPAN Phone: +81-22-217-7124, FAX: +81-22-263-9395, E-mail: bayashi@sse.ecei.tohoku.ac.jp

We have developed an advanced vector quantization (VQ) processor for a still image encoding systems. By utilizing needless calculation elimination method, computational cost of VQ encoding is reduced to 40% or less, while maintaining the accuracy of full-search VQ. We have successfully implemented the advanced VQ encoding method into a still image encoder. The VQ processor can compress still image of 1600 x 2400 pixels within one second, which is 60 times faster than currently PC.