

## SECOND ANNOUNCEMENT AND CALL FOR PAPERS

Sponsored by  
the Japan Society of Applied Physics and the IEEE Solid-State Circuits Society  
In Cooperation with the Institute of Electronics, Information and Communication Engineers  
and the IEEE Electron Devices Society

### 2003 SYMPOSIUM ON VLSI CIRCUITS

Rihga Royal Hotel Kyoto  
Kyoto, Japan  
June 12 - 14, 2003

The 2003 Symposium on VLSI Circuits will provide the designers of integrated circuits an opportunity to meet and present important new work at an international forum. The three day Symposium will continue the successful format and atmosphere established by the previous meetings, and partially overlap the 2003 Symposium on VLSI Technology, which is being held at the same hotel.

**The scope of the Symposium** covers all aspects of VLSI circuits, such as:

- digital, analog, and mixed digital-analog circuits: such as processors, ASICs, A/D and D/A converters, interface circuits, etc.
- memory circuits: such as static memory, non-volatile memory, cache memory, dynamic memory, and new concepts in memories based on quantum mechanics effects, magnetism, polymers, etc.
- system architecture, circuits and building blocks for networking applications
- circuits, functional blocks using quantum dots, MEMS, sensors, etc.
- fundamentals related to the above, including design automation tools, innovative circuits and device structures

**The emphasis** will be on circuit design. Papers will be considered on the basis of originality and quality. Implementation in a semiconductor chip and measured results are not mandatory. They are required only when feasibility of the presented circuit is not clear by simulations alone, such as in the case for most analog submissions. It is understood that papers in new areas are likely to contain less quantitative evaluations than those in more established areas.

#### SUBMISSION OF PAPERS

Your complete paper for the Circuits Symposium must occupy either two or four (never three) pages including illustrations. The choice of paper length (either 2 or 4 pages) is an option for authors whenever two pages is not enough to completely describe the major novelty and results of their work. The paper length will not be a criterion in selection of the papers for the program.

Those interested in submitting papers should contact the secretariat, as early as possible, to receive an author's kit. **The papers are to be submitted in final form and, if accepted, will be published as submitted.** The 2 camera-ready copies of your paper and 52 **DOUBLE-SIDED** copies are to be sent to the Business Center for Academic Societies Japan (all countries except North America and Europe) or to Widerkehr and Associates (North America and Europe).

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To improve the probability for acceptance of a submitted paper, the paper should describe original work with specific results from experiments or simulations. **The content of the paper must not be announced or published prior to the Symposium.**

**Deadline for Receipt of Papers is January 7, 2003**

#### INFORMATION AND REGISTRATION

Prospective attendees can obtain further information and forms for Symposium registration and hotel reservations by contacting their respective secretariats. Submissions from universities are encouraged. Partial travel expense support for students who are presenting papers is available upon request.

#### WORLD WIDE WEB

The VLSI Symposia have a web page. It contains general information on the Symposia. The address is:  
<http://www.vlsisymposium.org>

#### VLSI CIRCUITS SHORT COURSE

A one-day short course will be held on June 11, 2003. Details will be given in the VLSI Circuits Symposium Advance Program, which will be issued toward the end of March, 2003.

**Preceding this Symposium, the 2003 Symposium on VLSI Technology  
will be held at the same location. See the reverse side.**