## **Robust HfN Metal Gate Electrode for Advanced MOS Devices Application**

H.Y. Yu, H.F. Lim, J.H. Chen, M.F. Li, C.X. Zhu, D.-L. Kwong#, C.H. Tung\*, K.L. Bera\*, and C.J. Leo\* Silicon Nano Device Lab, ECE Department, National University of Singapore, Singapore 119260 (elelimf@nus.edu.sg); #Dept. of ECE, University of Texas at Austin, Austin, TX 78712, USA.; \*Institute of Microelectronics, Singapore 117685

## Abstract

A comprehensive study of HfN metal gate electrode for advanced MOS devices application is presented for the first time. It shows HfN is an excellent barrier against oxygen diffusion, has a midgap work function (~4.65 eV) on SiO<sub>2</sub>, and exhibits superior thermal stability with underlying gate dielectric. Negligible degradation in EOT, work function, leakage current, and TDDB upon high-temperature treatments (to 1000°C) has been observed in HfN gated MOS devices. These results suggest that HfN electrode is an ideal candidate for ultra thin body fully depleted SOI and symmetric double gate MOS applications.