

SESSION I – TAPA II
Plenary Session I

Thursday, June 15, 8:00 a.m.

Chairpersons: S. Kosonocky, IBM TJ Watson Research Center
K. Yano, Hitachi, Ltd.

8:00 a.m. Welcome and Opening Remarks
B. Gieseke, AMD
T. Kuroda, Keio University

1.1 – 8:15 a.m.

The Past, Present, and Future of Data Converters and Mixed Signal ICs: a “Universal” Model, David Robertson, Analog Devices

1.2 – 9:00 a.m.

Finger-Vein Authentication Technology and Its Future, Junichi Hashimoto, Hitachi, Ltd.