

GENERAL INFORMATION

DATE AND LOCATION

The 1999 Symposium on VLSI Technology and the 1999 Symposium on VLSI Circuits will be held on June 14–16 and June 17–19, 1999 respectively. The Technology Short Course will be held on June 13. The Circuits Short Course will be held on June 16. All the meetings will be held at Rihga Royal Hotel Kyoto, Horikawa-Shiokoji, Shimogyo-ku, Kyoto 600-8237, Japan.

TECHNICAL PROGRAMS AND SOCIAL EVENTS

Date	Morning and Afternoon	Evening
June 13 (Sun.)	VLSI Technology Short Course	Reception
14 (Mon.)	Symposium on VLSI Technology	Dinner
15 (Tue.)	Symposium on VLSI Technology	Rump Sessions
16 (Wed.)	Symposium on VLSI Technology VLSI Circuits Short Course	Joint Rump Session Reception
17 (Thur.)	Symposium on VLSI Circuits	Rump Sessions
18 (Fri.)	Symposium on VLSI Circuits	Dinner
19 (Sat.)	Symposium on VLSI Circuits (Morning)	

To encourage open discussion of technical information, **cameras, tape recorders etc. will not be allowed** in any sessions.

REGISTRATION DESK

The technical digest and registration materials will be distributed at the registration desk situated on the second floor of the Rihga Royal Hotel Kyoto. The registration desk will be open as follows:

Symposium on VLSI Technology

Saturday, June 12 6:00 p.m.–8:00 p.m.
Sunday, June 13 8:00 a.m.–7:00 p.m.
Monday, June 14 8:00 a.m.–5:00 p.m.
Tuesday, June 15 8:00 a.m.–5:00 p.m.
Wednesday, June 16 8:00 a.m.–5:00 p.m.

Symposium on VLSI Circuits

Tuesday, June 15 8:00 a.m.–5:00 p.m.
Wednesday, June 16 8:00 a.m.–5:00 p.m.
Thursday, June 17 8:00 a.m.–5:00 p.m.
Friday, June 18 8:00 a.m.–5:00 p.m.
Saturday, June 19 8:00 a.m.–12:00 noon

MESSAGE

Please check the message board periodically.

It will be located near the registration desk. Please advise those who wish to reach you during the day to phone the Rihga Royal Hotel Kyoto operator (+81-75-341-2311) and request the secretariat of VLSI Symposia (Room Horikawa).

REGISTRATION INFORMATION

All who wish to attend the 1999 VLSI Symposia and Short Courses should complete the enclosed Registration Form and send it **to the Japanese Secretariat or to the US Secretariat** with the registration fee. Advance registration is encouraged. The advance registration deadline is April 26, 1999.

(1) Payment in Japanese Yen

1. Bank transfer to VLSI Symposium.

Bank	Branch	Account No.	Account Name
Dai-ichi Kangyo Bank	Hongo (# 075) Tokyo	1161964 (ordinary)	VLSI Symposium

2. Bank draft or company check in Japanese Yen payable to the order of the 1999 VLSI Symposium. Personal checks are not acceptable.
3. Major credit cards.

(2) Payment in US Dollar

Payment in Japanese yen is strongly encouraged through the Japanese Secretariat by the use of internationally recognized credit cards. However, for special cases where payment in US dollars is more convenient or required, please remit payment through the US Secretariat. Please contact the US Secretariat concerning any unusual circumstances or methods of payments.

(3) On-site Registration

Only cash (Japanese Yen) or credit cards will be accepted at the on-site Registration Desk.

(4) Registration Fee

The symposium registration fee covers the symposium attendance, a copy of the technical digest, breakfasts, coffee breaks, reception and dinner. Student registration fee of each symposium does not include dinner. Extra copies of the technical digest (¥7,000) and dinner tickets (¥6,000) will be available at the registration desk for an additional fee.

	on and before April 26	on-site	Student
VLSI Technology Short Course	¥10,000 US\$100	¥15,000 US\$150	¥5,000 US\$50
Symposium on VLSI Technology	¥45,000 US\$450	¥50,000 US\$500	¥20,000 US\$200
VLSI Circuits Short Course	¥10,000 US\$100	¥15,000 US\$150	¥5,000 US\$50
Symposium on VLSI Circuits	¥40,000 US\$400	¥45,000 US\$450	¥15,000 US\$150
Symposia on VLSI Technology and Circuits	¥80,000 US\$800	¥90,000 US\$900	¥25,000 US\$250

(5) Cancellation: If written notice of cancellation reaches the Japanese Secretariat:

On and before May 20 Cancellation fee, ¥5,000 or US\$50, will be deducted before refunding the fee.

After May 20 No refund. A copy of the digest will be sent after the symposium.

VISA REQUIREMENT

All foreign participants must have a valid passport. We would like to advise all participants, who may be required to apply for a VISA, that the application process requires time. Please contact the nearest Japanese embassy as soon as possible.

TRAVEL EXPENSE SUPPORT

Requests for partial travel expense support for students who are presenting papers should be sent to:
Secretariat for 1999 VLSI Symposia (Japan)
c/o Business Center for Academic Societies Japan
5-16-9 Honkomagome, Bunkyo-ku, Tokyo 113-8622 Japan
no later than April 26, 1999.

WORLD WIDE WEB

The VLSI Symposia has a web page. It contains general information on the Symposia. The address is:

http://www.bcasj.or.jp/vlsi_sym/

SATELLITE WORKSHOPS

Preceding the 1999 VLSI Symposia, two workshops will be held at the same location. Details will be on the enclosed sheets.

June 12, 1999 **Fourth International Workshop on Statistical Metrology**

General Chair:

Masahiko Ogirima Hitachi
Tel: +3-5201-5065
Fax: +3-3270-5002
E-mail: ogirima@denshi.head.hitachi.co.jp

Arrangement Chair:

Naoyuki Shigyo Toshiba
Tel: +81-45-890-2801
Fax: +81-45-890-2890
E-mail: naoyuki-shigyo@toshiba.co.jp

June 12-13, 1999 **1999 Silicon Nanoelectronics Workshop** (<http://vlsi.iis.u-tokyo.ac.jp/si-nan>)

General Chair:

Akira Toriumi Toshiba
E-mail: toriumi@amc.toshiba.co.jp

Program Chair:

Toshiro Hiramoto Univ. of Tokyo
Tel: +81-3-3402-6231 EX.2370
Fax: +81-3-3402-0873
E-mail: hiramoto@nano.iis.u-tokyo.ac.jp

The registration desk will be open as follows:

Fourth International Workshop on Statistical Metrology

Saturday, June 12 8:00 a.m.-5:00 p.m.

1999 IEEE Silicon Nanoelectronics Workshop

Saturday, June 12 8:00 a.m.-5:00 p.m.

Sunday, June 13 8:00 a.m.-5:00 p.m.

Registration fee

	on and before April 26	on-site	Student
Statistical Metrology Workshop		¥18,000 US\$180	
Silicon Nanoelectronics Workshop	¥20,000 US\$200	¥25,000 US\$250	¥10,000 US\$100

Registration form is common to the form of the VLSI Symposia.