

PAPER PREPARATION KIT

for
The 1999 Symposium on VLSI Technology - June 14--16, 1999
and
The 1999 Symposium on VLSI Circuits - June 17--19, 1999

Dear Author(s):

Welcome to the 1999 Symposia on VLSI Technology & Circuits. We appreciate your interest in submitting a paper to the Symposium. Enclosed in this Paper Preparation Kit you will find all the necessary information and forms for the preparation of your submitted paper. **The abstracts are to be published in final form and, if accepted, will be published as submitted.**

For both Technology and Circuits Symposium:

At the 1999 Symposium on VLSI Technology, we will have a special session on "Technology for System LSI", which will be of great interest to both technology and circuit engineers. This session will deal with revolutionary process, device and circuit technologies for future system-on-silicon implementations. The Committee encourages prospective authors to submit papers on this subject, and will be in addition to the ordinary sessions, which will cover

For the Technology Symposium:

- new functional devices including quantum effect devices with possible VLSI implementation
- materials innovation for MOSFET and interconnect in VLSI
- advanced lithography and fine patterning technologies for high density VLSI
- process/device modeling and VLSI manufacturing control
- packaging and reliability of VLSI devices
- theories and fundamentals related to the above topics

For the Circuits Symposium:

- circuits for digital, analog, memory, communications and signal processing applications, including A/D and D/A converters, mixed analog/digital functions and interface circuits
- systems and architectures related to VLSI circuits
- fundamentals related to the above subjects, including innovative circuits and device structures

The emphasis will be on circuit design. Papers will be considered on the basis of originality and quality. Though the circuits need not necessarily be implemented in a semiconductor chip, measured results, particularly for analog submissions, are desirable. There will be several invited papers on emerging VLSI circuit technologies.

In order to insure that your paper receives a complete review, it is imperative that you follow all the instructions and submit all the required documentation. Enclosed to assist you are:

1. GENERAL GUIDELINES that address the deadlines and required documentation
2. AUTHOR'S GUIDE that provides a detailed description of camera-ready paper preparation
3. 75 WORD ABSTRACT FORMAT
4. COPYRIGHT TRANSFER FORM for Technology or Circuits giving the conference permission to print the paper if accepted

The deadline for RECEIPT of camera-ready papers is **January 8, 1999**. Your cover letter, camera-ready paper (plus 50 copies), 75 word abstract (plus 5 copies), and copyright transfer form **MUST ARRIVE** at the Conference Office by that date. **We are sorry but due to review deadlines, papers received after that date will not be accepted for review.**

If accepted for presentation, the content of the paper must not be announced or published prior to the Symposia. The Technical Program Committee will review all submitted papers and authors will receive notification of the Committee's decision around March 5, 1999.

We thank you for your interest in the Symposia on VLSI Technology & Circuits. If at any time a question exists, don't hesitate to contact the Conference Office at vlsisymp@bcasj.or.jp (Japan) or vlsi99@aol.com (USA).

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