

# **Fabrication of a novel strained SiGe:C-channel planar 55nm nMOSFET for High-Performance CMOS**

T. Ernst, J.-M. Hartmann, V. Loup, F. Ducroquet\*, P. Dollfus\*\*, G. Guegan, D. Lafond, P. Holliger, B. Prévitali, A. Toffoli, and S. Deleonibus  
CEA-LETI, 17 rue des Martyrs, 38054 Grenoble Cedex 9, France,  
\* LPM, INSA-Lyon, BP 69, 69621 Villeurbanne Cedex, France,  
\*\* IEF, Université Paris-Sud, 91405 ORSAY, France.

We present for the first time strained epitaxially grown Si:C and SiGe:C channel NMOS devices compatible with a standard 50nm CMOS process flow. The advantages of this new architecture for CMOS integration are a highly retrograde channel doping profile and a suppression of boron diffusion and Oxidation Enhanced Diffusion, leading to a dramatic decrease of short channel effects. Mobility in the Si:C and SiGe:C inversion layer is characterized for the first time (77K to 300K) and optimized.