

## SECOND ANNOUNCEMENT AND CALL FOR PAPERS

Sponsored by the Japan Society of Applied Physics and the IEEE Electron Devices Society  
In Cooperation with the IEEE Solid-State Circuits Society

### 2003 SYMPOSIUM ON VLSI TECHNOLOGY

**Rihga Royal Hotel Kyoto**  
**Kyoto, Japan**  
**June 10 - 12, 2003**

The 2003 Symposium on VLSI Technology welcomes submission of papers on all aspects of VLSI technology. The scope of the symposium is:

- new concepts and breakthroughs in VLSI devices and processes
- new functional devices including quantum effect devices with possible VLSI implementation
- materials innovation for MOSFET and interconnect in VLSI
- advanced lithography and fine patterning technologies for high density VLSI
- process/device modeling of VLSI devices
- packaging and reliability of VLSI devices
- theories and fundamentals related to the above devices
- new concepts and technologies for VLSI manufacturing

#### SUBMISSION OF PAPERS

Prospective authors must submit papers in the format of two pages of camera-ready copy. Those interested in submitting papers should contact the secretariat, as early as possible, to receive an author's kit. ***The papers are to be submitted in final form and, if accepted, will be published as submitted.*** The 2 camera-ready copies of the two page paper and 52 DOUBLE-SIDED copies are to be sent to the Business Center for Academic Societies Japan (all countries except North America and Europe) or to Widerkehr and Associates (North America and Europe).

#### *Secretariat for VLSI Symposia (Japan)*

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To improve the probability for acceptance of a submitted paper, the paper should describe original work with specific results from experiments or simulations. **The content of the paper must not be announced or published prior to the Symposium.**

**Deadline for Receipt of Papers is January 7, 2003**

#### INFORMATION AND REGISTRATION

Prospective attendees can obtain further information and forms for Symposium registration and hotel reservations by contacting their respective secretariats. Submissions from universities are encouraged. Partial travel expense support for students who are presenting papers is available upon request.

#### WORLD WIDE WEB

The VLSI Symposia have a web page. It contains general information on the Symposia. The address is:

**<http://www.vlsisymposium.org>**

#### VLSI TECHNOLOGY SHORT COURSE

A one-day short course will be held on June 9, 2003. Details will be given in the VLSI Technology Symposium Advance Program, which will be issued toward the end of March, 2003.

#### SATELLITE WORKSHOP

Satellite workshop will be held at the same location.

June 8-9      2003 Silicon Nanoelectronics Workshop

**Following this Symposium, the 2003 Symposium on VLSI Circuits  
will be held at the same location. See the reverse side.**