2014 SYMPOSIA ON VLSI TECHNOLOGY & CIRCUITS

Semiconductor industry's premier event on advances in microelectronics technology & circuits

Hilton Hawaiian Village
HONOLULU
June 9-13, 2014

2014 VLSI SYMPOSIA HIGHLIGHTS

The 2014 VLSI Symposia technical program consists of overlapping sessions from June 9 – 12 (Technology) and June 10 – 13 (Circuits), with more than 200 presentations, short courses and panel discussions by the leading researchers and scientists. Program is designed to highlight recent advances in microelectronics technology and circuits, and promote networking amongst participants.

SYMPOSIUM ON VLSI TECHNOLOGY

PLENARY PRESENTATIONS
Tuesday morning, June 10

Customer Value Creation
in the Information Explosion Era
– Keiichiro Shimada, Sony Corporation

RUMP SESSION (panel discussion)
Tuesday evening, June 10

450mm, EUV, III-V, 3D – All in 7nm?
Are You Serious?!
– Andrzej Strojwas, PDF Solutions

TECHNOLOGY FOCUS SESSIONS

Non-volatile Embedded Memory

Interconnect: Local & Global

JOINT PROGRAM HIGHLIGHTS

JOINT FOCUS SESSIONS

- Design in Scaled Technologies
- Design Enablement
- Memory Technology & Design
- 3D (TSV) & Heterogeneous Integration

JOINT RUMP SESSION (panel discussion)
Tuesday evening, June 10

Who Gives Up on Scaling First:
Device & Process Technology Engineers,
Circuit Designers or Company Executives?
– Elad Alon, UC Berkeley / Yee Chia Yeo, NUS

JOINT BANQUET
Wednesday, June 11

SYMPOSIUM ON VLSI CIRCUITS

PLENARY PRESENTATIONS
Wednesday morning, June 11

Storage Architecture for the Data Center in 2020
– Ed Doller, Micron Technology

Technology Development for
Printed LSIs Based on Organic Semiconductors
– Jun Takeya, University of Tokyo

RUMP SESSIONS (panel discussions)
Thursday evening, June 12

What Should Circuit
Designers do in an Era of System Level Design?
– Jan Rabaey, UC Berkeley

Lessons & Challenges for
Future Mixed-Signal, RF & Memory Circuits
– Tom Lee, Stanford / Nicky Lu, Etron

PROFESSIONAL DEVELOPMENT AT THE VLSI SYMPOSIA

SHORT COURSES

High Performance Mobile SoCs
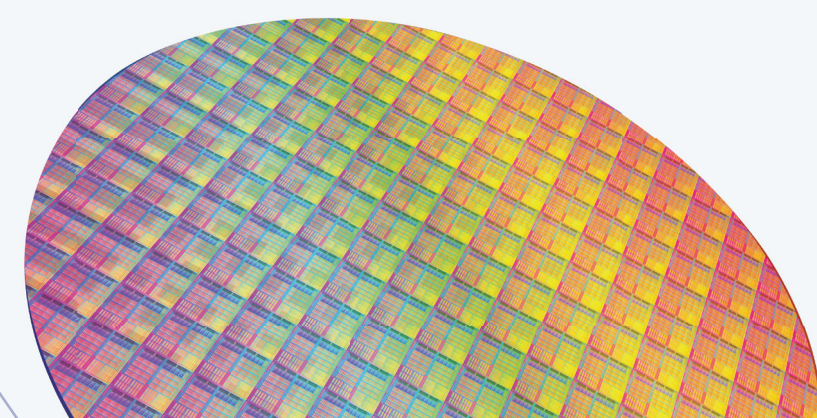Enabled by 10nm SoC Technology (Technology)
– Monday, June 9

Advanced Data Converter &
Mixed-Signal Circuit Design (Circuits)
– Tuesday, June 10

Advanced Energy-Efficient
Digital Design (Circuits)
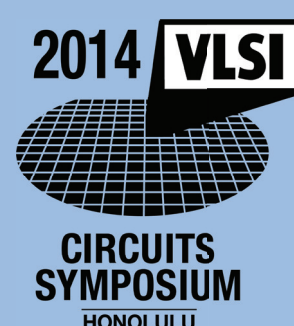– Tuesday, June 10

SATELLITE WORKSHOPS

IEEE Silicon Nanoelectronics Workshop
– June 8&9

Spintronics Workshop
– June 9

For complete conference and registration
information visit: **http://www.vlsisymposium.org/**

2014 VLSI
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SSCS

IEEE

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