

1999 Symposium on VLSI Circuits Short Course “System LSI and the Related Design Technology”

Wednesday, June 16 (Suzaku)

Short Course Organizer: T. Enomoto, *Chuo Univ.*

Chairman: Y. Sugimoto, *Chuo Univ.*

9:30 Opening

9:40 Single Chip MPEG2 Encoder

E. Ogura, *Sony US Research Laboratories*

10:25 Processor Design for Multimedia Processing

T. Arai, *NEC*

11:10 Embedded DRAM Architecture

T. Watanabe, *Hitachi*

11:55 Lunch

13:15 Digital Signal Processors for Mobile Phone Terminals

K. Ueda, *Matsushita*

14:00 Low Power CMOS Digital Design

T. Kuroda, *Toshiba*

14:45 Clocking Methods for High Performance LSI Systems

S. Butler, *AMD*

15:30 Break

15:50 Interconnect structure and circuit design for system LSI

K. Soumyanath, *Intel*

16:35 Conclusion

1999 VLSI Technology Short Course Program

Sunday, June 13, 1999 (Suzaku)

Organizers: T. Nishimura, *Mitsubishi Electric*
S. Wong *Stanford Univ.*

“Embedded Technologies for System on a Chip”

8:55 Introduction

T. Nishimura, *Mitsubishi Electric*

9:00 Embedded Design Techniques for System on a Chip

T. Iizuka, *THine Electronics*

10:30 Break

10:45 Embedded DRAM

C. Wang, *TSMC*

12:15 Lunch

13:15 Embedded Flash

T. Iwazaki, *Fujitsu*

14:45 Break

15:00 Embedded Analog/RF

K. Ng, *Lucent Bell Labs.*