

# Characterization and Comparison of High-k Metal-Insulator-Metal (MiM) Capacitors in 0.13 $\mu\text{m}$ Cu BEOL for Mixed-Mode and RF Applications

Y.L. Tu, H.L. Lin, L.L. Chao, Danny Wu, C. S. Tsai, C. Wang, C.F. Huang\*, C.H. Lin\*, Jack Sun\*

Memory Technology Division, \*Logic Technology Division,

Taiwan Semiconductor Manufacturing Corp. (TSMC)

No.25 Li-Hsin Rd., Science-Based Industrial Park, Hsin-Chu, Taiwan

TEL: +886-3-5678888, Fax: +886-3-5662059, e-mail: yltu@tsmc.com.tw

## Abstract

In this paper, we report high-k MiM capacitors including  $\text{Ta}_2\text{O}_5$ ,  $\text{TaO}_x\text{N}_y$ ,  $\text{HfO}_2$ ,  $\text{Al}_2\text{O}_3$  and  $\text{Ta}_2\text{O}_5/\text{Al}_2\text{O}_3$  stack layer integrated in 0.13  $\mu\text{m}$  8-level Cu-metallization technology using Cu barrier as both top and bottom electrodes.  $\text{Ta}_2\text{O}_5$  exhibits excellent voltage and temperature linearity of capacitance.  $\text{Al}_2\text{O}_3$  shows low leakage, but poor voltage and temperature linearity. Voltage linearity could be significantly affected by high-k deposition temperature. We present high-k MiM capacitors with voltage linearity as low as 25 ppm/V and 13 ppm/V<sup>2</sup>.