The Emerging Horizons of Computing Explored in *"Inflections in VLSI Technology – Cloud & Beyond"*

Short course at 2016 Symposia on VLSI Technology & Circuits provides an intriguing look at the converging technology paths to the future of computing…

HONOLULU, HI (MAY 11, 2016) – High performance computing is reaching a threshold marked by multiple technology and system-infrastructure trends, which have the potential to create a paradigm shift in the industry. Scheduled for June 13 in advance of the 2016 Symposia on VLSI Technology & Circuits, a full-day short course by leading industry and academic experts entitled *"Inflections in VLSI Technology – Cloud & Beyond"* will explore these significant technology trends that will define the future of computing.

The short course is part of the Symposia's overall theme **"Inflections for a Smart Society,"** reflecting the semiconductor industry's transition as scaling slows and integration technologies rise to meet the challenges of an increasingly connected society (Internet of Things).

The full-day short course enables participants to fully explore this subject in sessions that cover high performance computing, silicon photonics, memory technologies, cloud computing and novel power devices; as well as system-level topics such as ultra-low energy consumption, advanced memory architectures, neuromorphic computing technology, flexible sensors and increased battery life:

Session 1 – Cloud Computing
   • *The Future of HP Computing – Technology Scaling & Hardware Accelerators*, by Terence Hook, IBM
   • *Silicon Photonics Technology Enabling Both Low Power Consumption & High Aggregated Bandwidth*, by Ken Morito, Fujitsu
   • *The Confluence of Memory & Storage technologies*, by Craig Hampel, Rambus
   • *From Data Center to Mobile Phone – System Architecture Influences on Semiconductor technology Requirements*, by Ted Letavic, GLOBALFOUNDRIES
   • *Cloud Computing – Everything Close with 3D System Integration*, by Denis Dutoit, CEA-LETI
   • *Performance & Application of Power Devices in Si, SiC & GaN*, by Salvo Coffa, STMicroelectronics

Session 2 – The Beyond – Edge Computing & Emerging Horizons
   • *The Road to Ultra-Low Energy Computation*, by Jan Rabaey, UC Berkeley

• *The Challenge of MOS/MTJ-Hybrid Nonvolatile VLSI Processor for IoE Applications*, by Takahiro Hanyu, Tohoku University
　　• *Recent Progress in Neuromorphic Computing*, by Geoffrey Burr, IBM
　　• *Flexible Sensors for Health Monitoring Applications*, by Takao Someya, U. of Tokyo
　　• *Battery Life Considerations for Mobile PCs*, by Kamal Shah, Intel

Separate registration is required for short course participation. Complete information about the program is available here:
http://vlsisymposium.org/vlsi-technolog/

The 2016 Symposia on VLSI Technology & Circuits will be held at the Hilton Hawaiian Village, Honolulu, Hawaii from June 13-16, 2016 (Technology) and June 15-17, 2016 (Circuits). Held together since 1987, the Symposia provide a unique opportunity for the world's top device technologists, circuit and system designers to exchange leading edge research on microelectronics technology, with alternating venues between Hawaii and Japan.

Sponsoring Organizations
The Symposium on VLSI Technology is sponsored by the IEEE Electron Devices Society and the Japan Society of Applied Physics, in cooperation with the IEEE Solid State Circuits Society.

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Further Information, Registration and Program Details
Visit: http://www.vlsisymposium.org.
Media Contacts
Chris Burke | BtB Marketing Communications
chris.burke@btbmarketing.com | +1-919-872-8172
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