The IEEE Solid-State Circuits Society

VLSI Circuits Symposium Student Travel Grant Award

Application Deadline: April 30, 2017

The IEEE Solid-State Circuits Society (SSCS) will offer several travel grants to help student members who would otherwise not be able to attend VLSI Circuits Symposium 2017 in Kyoto. These awards will provide stipends of up to USD $400 each. The winners will be notified by email by the 15th May 2017. Eligibility Requirements Applicants must be members of the Solid-State Circuits Society, have not previously received a Student Travel Grant Award, have been enrolled in a Ph.D. program for at least one year, and be fulltime students at the time of their request.

**Application**

1. Biographical Information

a. Name

b. Mailing Address

c. Email address

d. IEEE member number

e. Name of School and Department

f. Name of advisor or major professor

g. Email address of advisor

h. Grade point average in graduate level courses: \_\_\_\_\_\_\_ out of a possible total of \_\_\_\_\_\_\_\_\_\_\_\_\_ . (Example: 3.5 out of 4.0)

2. Academic Profile

A one-page summary (up to 250 words) of academic background, including degrees granted and grade point average of each; PhD research topic; reason(s) for wanting to attend the Conference.

3. Recommendation

A one-page recommendation (up to 250 words) from your advisor or major professor. Please note that he or she may recommend only one student in a given year.

**Submission**

Please email all the above information in one PDF file to **vlsisymp@jtbcom.co.jp** no later than **May 15, 2017**.